

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number		10026583	
	Filing Date		2001-12-18	
	First Named Inventor		Cometto	
	Art Unit		2143	
	Examiner Name		Jean Gilles	
	Attorney Docket Number		ANDIP002/9008	

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<i>TL</i>	1	Australian Office Action, Application No. 2002359740, mailed April 11, 2007 (Atty. Dkt: ANDIP002.AU)	<input type="checkbox"/>
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Examiner Signature	<i>Jean Gilles</i>	Date Considered	<i>08/03/07</i>
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Form 1449 (Modified)		Atty Docket No. ANDIP002	Application No.: 10/026,583
Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)		Applicant: Cometto, et al.	Group 2857
		Filing Date December 18, 2001	

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
SF	A	6,295,575	09/25/01	Blumenau, et al.			06/29/98
	B	6,269,431	07/31/01	Dunham			08/13/98
	C	6,269,381	07/31/01	St. Pierre, et al.			06/30/98
	D	6,266,705	07/24/01	Ullum, et al.			09/29/98
	E	6,260,120	07/10/01	Blumenau, et al.			06/29/98
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
SF	U	Tamir Y, Frazier G., <i>High Performance Multi-Queue Buffers For VLSI Communications Switches</i> , Proc. Of 15 th Ann. Symp. On Comp. Arch., pp.343-354, June 1988
JL	W	Andrew S. Tenenbaum, <i>Computer Networks</i> , Third Edition ISBN 0-13-349945-6, © 1996 Prentice Hall



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